

17698 U.S. PTO  
06/20/03

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17399 U.S. PTO  
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06/20/03

June 20, 2003

MAIL STOP PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
PO Box 1450  
Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Umesh Nair  
Title: System for Optimizing Buffers in Integrated Circuit Design Timing Fixes  
Docket No.: P8987

- This Return Receipt Postcard  
 Transmittal Letter  
 10 page(s) Specification (not including Claims)  
 6 page(s) Claims  
 1 page(s) Abstract  
 6 sheet(s) of Drawings  
 2 page(s) Declaration For Patent Application and Power of Attorney  
 1 page(s) Recordation Form Cover Sheet  
 1 page(s) Assignment  
 Check for Filing Fees and Recordation of Assignment Fees

#### CLAIMS AS FILED (fees computed under §1.9(f))

For	Number <u>Filed</u>	Number <u>Extra</u>	Rate	\$	Basic Fee
Total Claims	24	-20 =	4 x \$18	=	\$ 750.00
Independent Claims	3	-3 =	0 x \$84	=	\$ 0.00

- Fee for filing the patent application in the amount of \$ 822.00  
 Check enclosed for fee for filing the patent application and assignment in the amount of: \$ 862.00  
 The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 502264

EXPRESS MAIL LABEL NO:

EV324251845US

Respectfully submitted,

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